



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
POIROUX ET AL.)
)
Serial No. **10/672,931**)
)
Filing Date: **SEPTEMBER 26, 2003**)
)
For: **PROCESS AND DEVICE FOR**)
EVALUATING A CMOS LOGICAL CELL)
_____)

TRANSMITTAL OF FORMAL DRAWINGS

Mail Stop: PGPUB-Drawings
Commissioner for Patents
Alexandria, VA 22313-1450

Sir:

Enclosed are eight (8) sheets of formal drawings to be
filed in the above-identified patent application.

Respectfully submitted,

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CERTIFICATE OF MAILING

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